

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**(Under 37 CFR 1.97(b) or 1.97(c))**

Docket No.

SEC.1052

In Re Application Of: **Chan-kyung KIM**

DEC 15 2004

Application No.

Filing Date

Examiner

Customer No.

Group Art Unit

Confirmation No.

10/698,383

3 November 2003

NGUYEN, Minh T.

2816

2722

Title: **DELAYED TAP SIGNAL GENERATING CIRCUIT FOR CONTROLLING DELAY BY
INTERPOLATING TWO INPUT CLOCKS**

Address to:

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. ☐ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☒ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☒ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).

3. ☒ Also enclosed is a copy of an Office Action dated 23 September 2004 issued by the Korean Patent Office in connection with Application No. 10-2002-0068155 wherein the enclosed reference was cited.

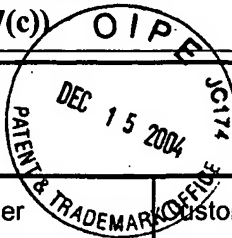
TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

(Under 37 CFR 1.97(b) or 1.97(c))

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In Re Application: Chan-kyung KIM



Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/698,383	3 November 2003	NGUYEN, Minh T.		2816	2722

Title: **DELAYED TAP SIGNAL GENERATING CIRCUIT FOR CONTROLLING DELAY BY
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Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 50-0238 as described below.
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Dated: 15 December 2004

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(703) 715-0870

CC:

**STATEMENT UNDER 37 CFR 1.97(e) ACCOMPANYING
INFORMATION DISCLOSURE STATEMENT**

Docket No.
SEC.1052

In Re Application Of: **Chan-kyung KIM**

Application No.

10/698,383

Filing Date

3 November 2003

Examiner

NGUYEN, Minh T.

Customer No.

Group Art Unit

2816

Confirmation No.

2722

Invention: **DELAYED TAP SIGNAL GENERATING CIRCUIT FOR CONTROLLING DELAY BY
INTERPOLATING TWO INPUT CLOCKS**

COMMISSIONER FOR PATENTS:

This is a statement under the provisions of 37 CFR 1.97(e) in the above-identified application.

Check applicable statement herebelow:

Statement Under 37 CFR 1.97(e)(1)

- ☒ Each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

Statement Under 37 CFR 1.97(e)(2)

- ☐ No item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned person, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.



Signature

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Dated: **15 December 2004**

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	1
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PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Complete if Known

Application Number	10/698,383
Filing Date	11/03/2003
First Named Inventor	Chan-kyung Kim
Art Unit	2816
Examiner Name	NGUYEN, Minh T.
Attorney Docket Number	SEC.1052

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY			
		JP 2002232274	08-16-2002	Nippon Electric		✓

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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